

DSE 通讯协议中 lead 和 lag 的说明

Register offset	Name	Minimum value	Maximum value	Scaling factor	Units	Bits/ Sign
60-61	Bus L2 Var	-99,999,999	99,999,999	1	Var	32S
62-63	Bus L3 Var	-99,999,999	99,999,999	1	Var	32S
64-65	Bus total Var	-999,999,999	999,999,999	1	Var	32S
66	Bus power factor L1	-1	1	0.01		16S
67	Bus power factor L2	-1	1	0.01		16S
68	Bus power factor L3	-1	1	0.01		16S
69	Bus average power factor	-1	1	0.01		16S
70	Bus percentage of full power (8660)	-999.9	+999.9	0.1	%	16S
71	Bus percentage of full Var	-999.9	+999.9	0.1	%	16S
72-73	Load total watts	-999,999,999	999,999,999	1	W	32S
74-75	Load total Var	-999,999,999	999,999,999	1	Var	32S
76	Mains R.O.C.O.F.	0	10.00	0.01	Hz/s	16
77	Mains vector shift	0	360.0	0.1	Degrees	16
78	Gen L1 lead /lag	0	2			16
79	Gen L2 lead /lag	0	2			16
80	Gen L3 lead /lag	0	2			16

偏移地址 78/79/80，分别是发电机 L1/ L2/ L3 的 lead 或 lag，可通过第三方软件读此寄存器的内容，内容分别是 0/1/2，代表内容如下：

0: leading (超前): 电流相位领先于电压，一般带容性负载

1: indeterminate,未确定

2: lagging (滞后): 电流相位落后于电压，一般带感性负载